## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Confirmation No.: 6007 Kenichi ADACHI

Group Art Unit: Not Yet Assigned

Application No.: 10/589,380 Examiner: Not Yet Assinged

Filing Date: August 15, 2006

Attorney Docket No.: 545/73

For: Processor, Multiprocessor System, Processor

System, Information Processing Apparatus, and

Temperature Control Method

## INFORMATION DISCLOSURE STATEMENT

## **VIA EFS**

Mail Stop: Amendment Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant respectfully submits this Information Disclosure Statement, identification of the information, and copies thereof if required pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 in order to comply with the duty of disclosure. The enclosed Form PTO SB/08A/B identifies the references of which Applicant is aware. The Examiner is respectfully requested to consider the information herein and make it of record in the subject application.

By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b) within three months of the filing date of the application or before the application has received a substantive Office Action. Accordingly, no fee is necessary; however, should the Patent Office determine that a fee is due, the Commissioner is authorized to charge any such fee to Deposit Account No. 11-0223.

By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(c) before the mailing date of a Final Action, a Notice of Allowance, or close of prosecution. Accordingly included herewith is:

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| Respectfully submitted,   |
| By: s/Matthew B. Dernier/ Matthew B. Dernier Registration No. 40,989 KAPLAN GILMAN GIBSON & DERNIER LLP 900 Route 9 North Woodbridge, New Jersey 07095 Telephone (732) 634-7634 Attorneys for Applicant |
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|                                   | Application Number     | 10/589,380       |  |  |
| INFORMATION DISCLOSURE            | Filing Date            | August 15, 2006  |  |  |
|                                   | First Named Inventor   | Kenichi ADACHI   |  |  |
| STATEMENT BY APPLICANT            | Art Unit               | Not Yet Assigned |  |  |
| (Use as many sheets as necessary) | Examiner Name          | Not Yet Assigned |  |  |
| Sheet 1 of 2                      | Attorney Docket Number | 545/73           |  |  |

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| Examiner<br>Initials* | Cite<br><b>N</b> o. <sup>1</sup> | Document Number  Number-Kind Code <sup>2 (if known)</sup> | Publication D<br>MM-DD-YY |                     | Name of Patentee or<br>pplicant of Cited Document  | Pages, Columns, Lines, Where<br>Relevant Passages or Relevan<br>Figures Appear |
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| Examiner<br>Initials* | Cite<br>No.1                     | Foreign Patent Document                                   |                           | Publication<br>Date | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns, Lines,<br>Where Relevant Passages                              |

| Examiner<br>Initials* | Cite<br>No.1 | Foreign Patent Document  | TOREIGN PATENT DOCUMENTS  It Document Publication Name of Patente  Date Applicant of Cited D |                            | Pages, Columns, Lines,<br>Where Relevant Passages |                    |
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| iiiidai3              | NO.          | Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (# known) | MM-DD-YYYY   | Applicant of orea bosoment | Or Relevant Figures Appear                        | T'                 |
| /M.Z./                | ВА           | JP 2000-035985*  | 02-02-2000   | Denso Corp.                |   | $oxed{\checkmark}$ |
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| Examiner  | (3.5           | Date       | 20/05/02/2 |
|-----------|----------------|------------|------------|
| Signature | /Meng Yao Zhe/ | Considered | 03/25/2012 |

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<sup>\*</sup>English Translation of Abstract only.

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|           |                      |           |           | Application Number     | 10/589,380       |  |  |
| INF       | ORMATION             | DIS       | CLOSURE   | Filing Date            | August 15, 2006  |  |  |
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|           | (Use as many she     | ets as n  | ecessary) | Art Unit               | Not Yet Assigned |  |  |
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| Examiner<br>Initials* | Cite<br>No. <sup>1</sup> | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T <sup>2</sup> |
| /M.Z./                | CA                       | International Preliminary Report on Patentability to corresponding PCT/JP2004/019208, dated 11/29/2006, 5 pgs.  | ✓              |
| /M.Z./                | СВ                       | BENINI et al.; "Reducing Power Consumption of Dedicated Processors Through Instruction Set Encoding;" VLSI, 1988, Proceedings of 8th Great Lakes Symposium on IEEE, pp. 8-12.   | 1              |
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| Examiner  |                | Date       | 00/05/0040 |
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| Signature | /Meng Yao Zhe/ | Considered | 03/25/2012 |

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<sup>1</sup> Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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